FIG. 1A

Fan Subsystem 110

120 Line Cards

140 Data Fabric Cards

150 DSR Fabric Cards

DSR Cards 170

160 Management Card
+ Redundant

Fan Subsystem 110

100

FIG. 1B

Fan Subsystem 110

130 (16) I/O Cards

48v DC Power Entry 115
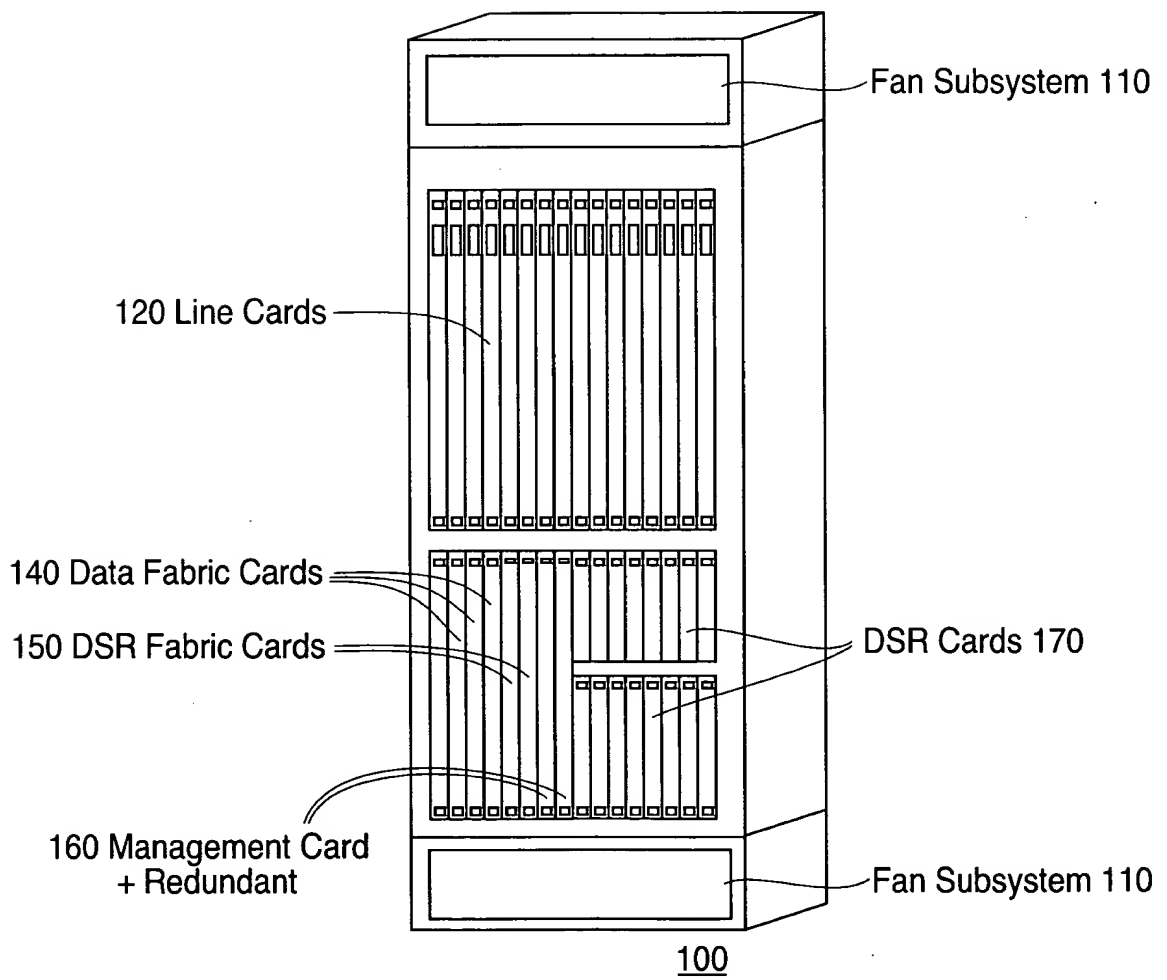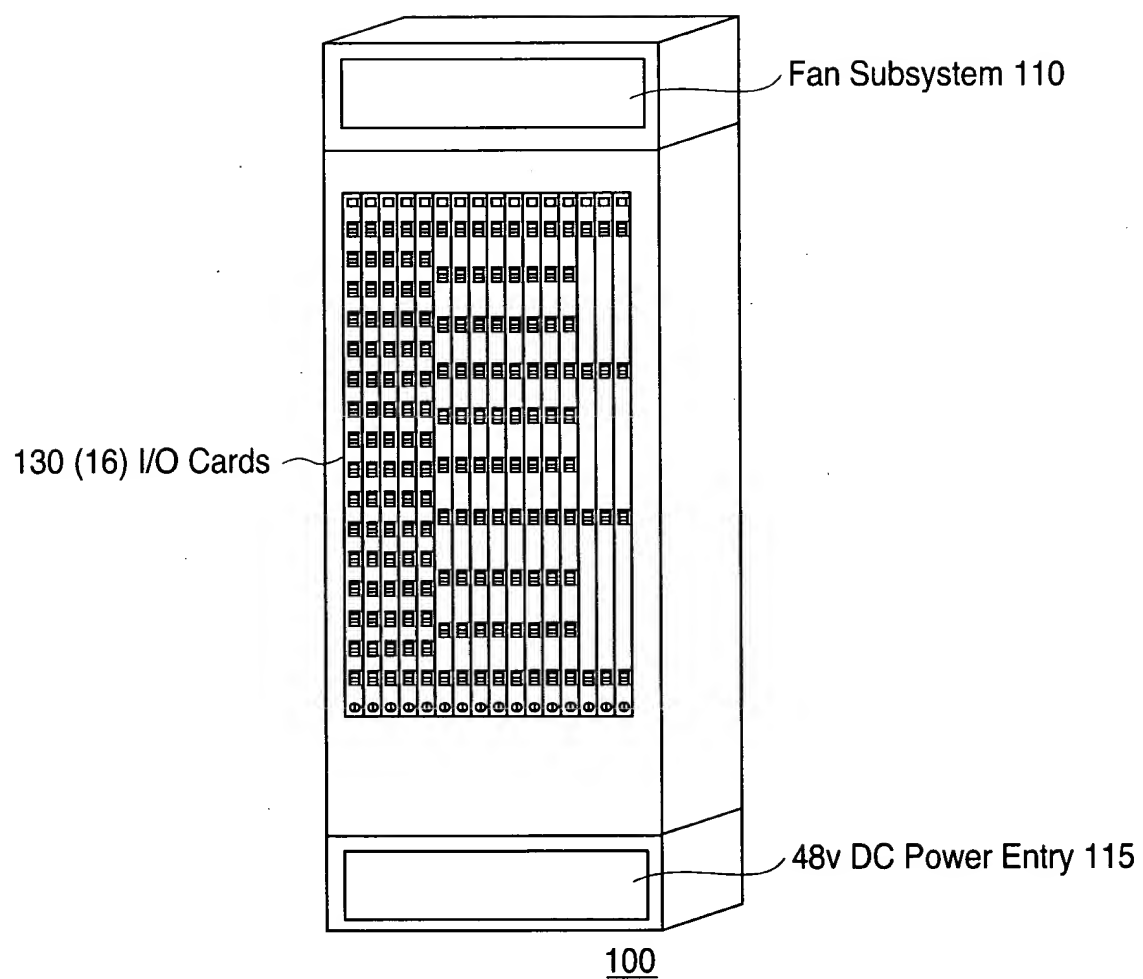
100

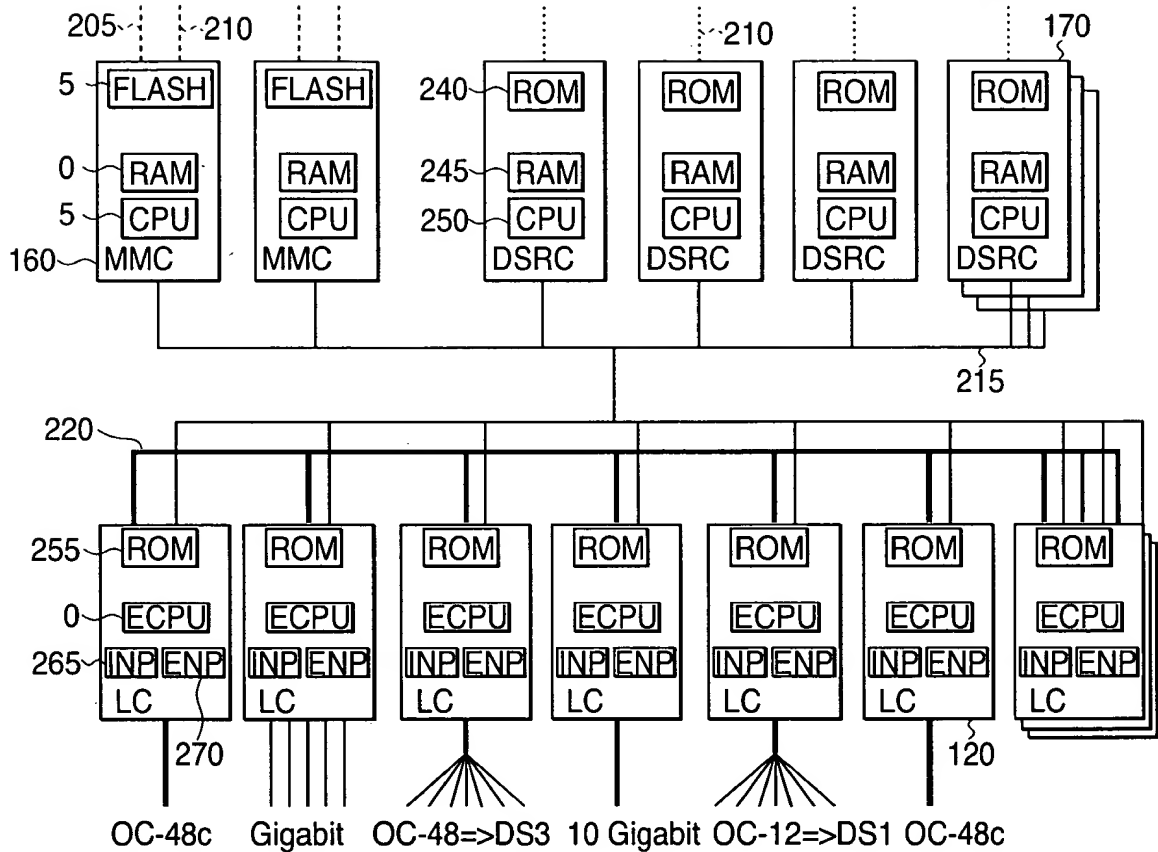# FIG. 2

OC-48c   Gigabit OC-48=>DS3 10 Gigabit OC-12=>DS1 OC-48c

MMC   - Management Module Card
DSRC - Distributed Service Router Card
CPU   - the main CPU for DSRC, could be a PowerPC
LC   - Line Card
FLASH - to hold image and configuration files, serve as Booter as well
ROM   - BootROM
ECPU - the Exception Processor, could be the same as the CPU for DSRC
INP   - the Ingress Network Processor for packet processing, including
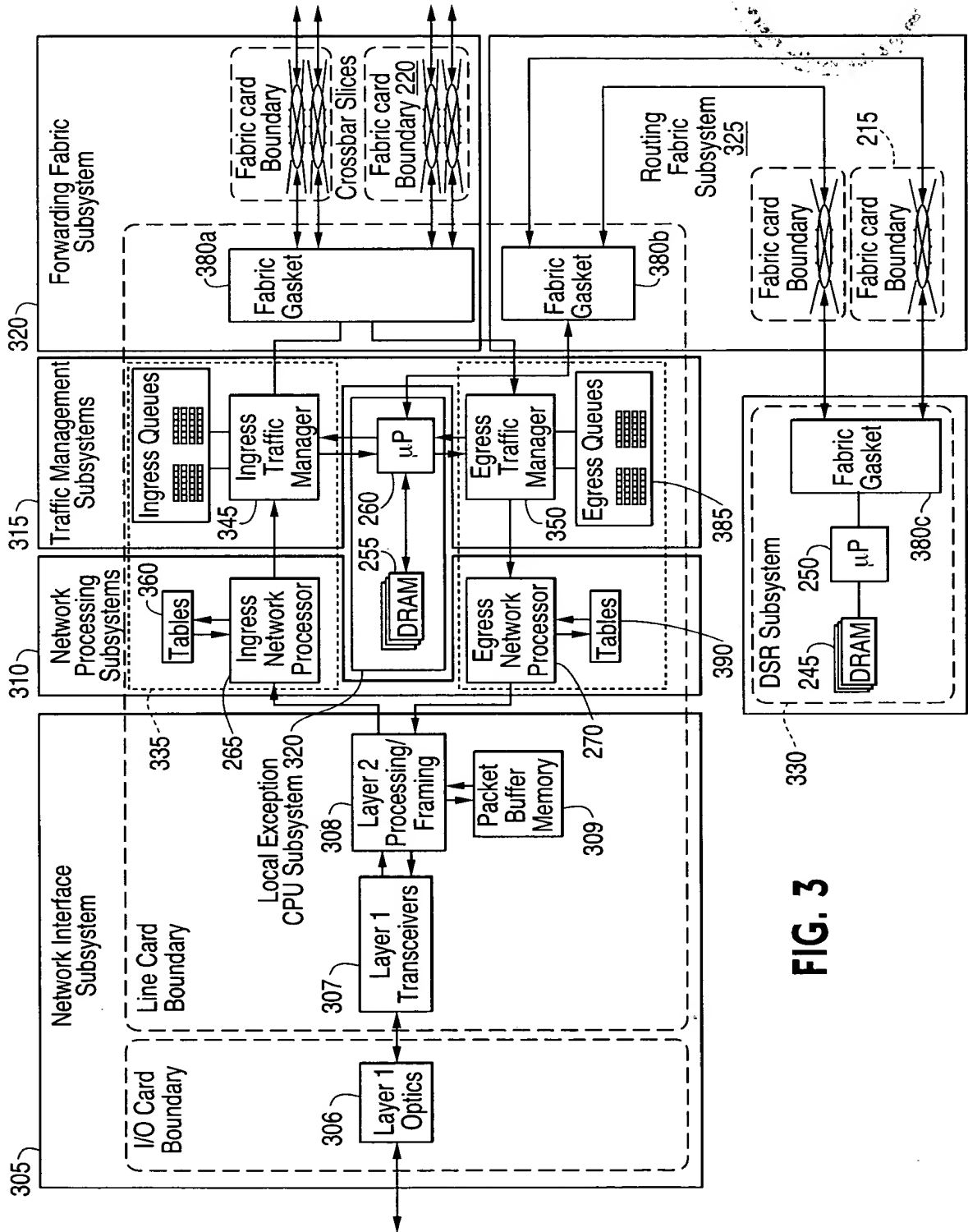   co-processor and custom ASICs
ENP   - the Egress Network Processor

`..........`   the console port
`--------`   the 10/100M Ethernet port for out of band management
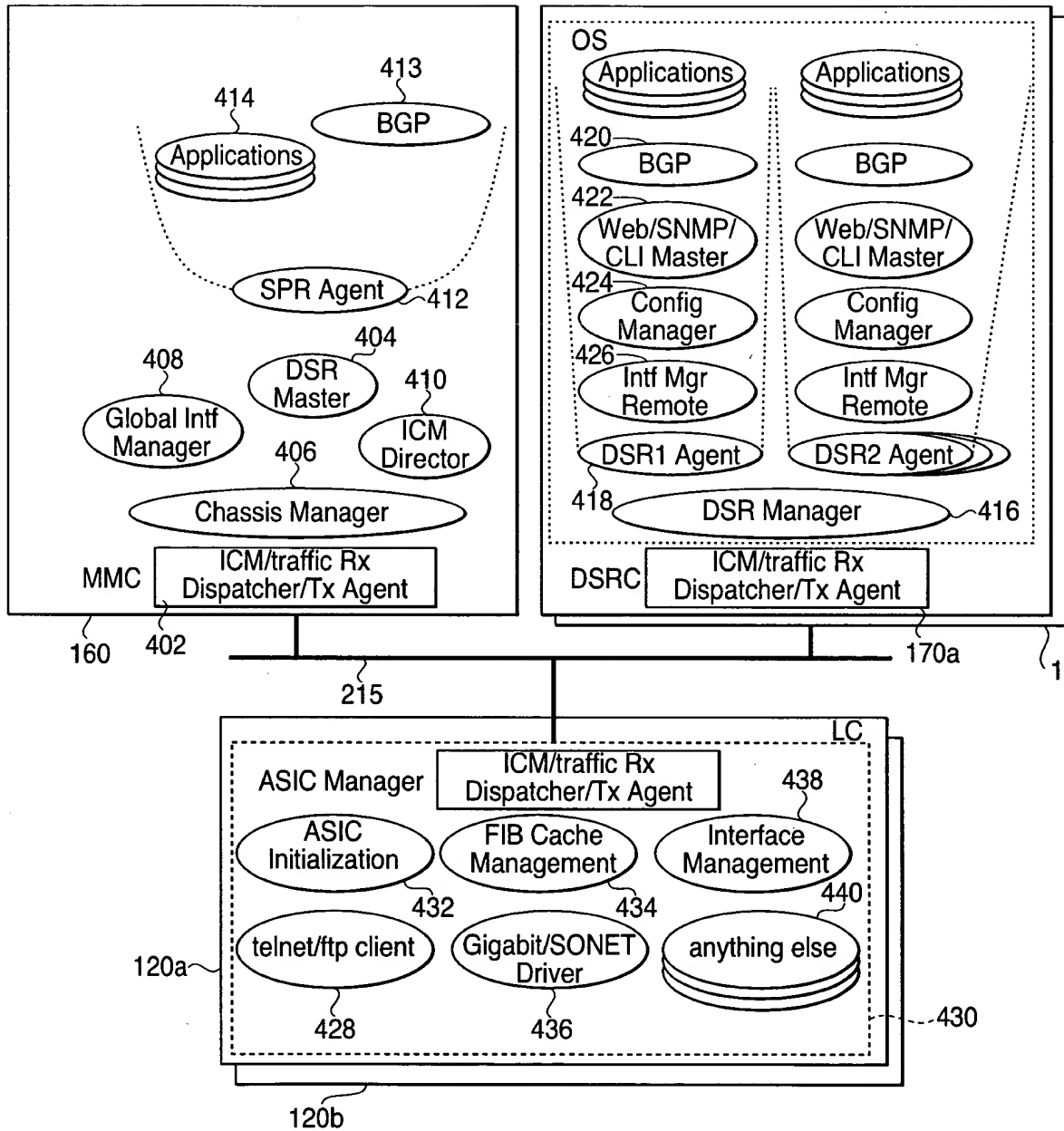`———`   the line bus or lower speed fabric connecting between the SPRC and DSRC,
           and between the Line Cards and DSRC/SPRC. This is mainly for ICM.
`▬▬▬`   the high speed fabric for the switch traffic

FIG. 3

FIG. 4

FIG. 5

PRIMARY DSR

SECONDARY DSR

ICM EVENTS

SYNCHRONIZATION EVENTS